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Inventor: OVCHINNIKOV VALERY VASILIEVICH [RU]
Applicant: OVCHINNIKOV VALERY VASILIEVICH [RU]
EC: H04B3/30; H04B3/50; (+1)
IPC: ***H04B3/50; H04B3/30; H04B3/54;*** (+4)
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Svensson, C.; Jiren Yuan;
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1. METHOD FOR TRANSMITTING DIGITAL ELECTRIC SIGNALS
Ovchinnikov, Valery Vasilievich (Ovchinnikov, Valery Vasilie
APPLICATION, Jun 2005
patno:EP1548954
...separate **power supply** of the devices...disposed on a **two-wire** with...connected to the **second pole** of the source...permits the **p** devices...disposed on a **two-wire communication** line with...con of the source...pole of the **power supply** source is...wire of the **tw** line is connected to the **second pole** via a resistor...
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...pole of the **power supply** is connected...wire of the **two-wire c** connected to the **second pole** of the **power supply** via a resistor. **communication** line with...pole of the **power supply** and the firs **pole** of the **power**...
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1. Circuit for attenuation of echos caused by line variations and an int capacitively coupling a plurality of sources to a two-wire communic
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A circuit attenuates echo caused by line variations and a transform two-wire line interface. To reduce echo, a test tone is introduced o with all station units connected to the line. The resultant receive ...
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2. METHOD FOR TRANSMITTING DIGITAL ELECTRIC SIGNALS
Ovchinnikov, Valery Vasilievich (Ovchinnikov, Valery Vasilie *APPLICATION*, Jun 2005
patno:EP1548954
...the separate **power supply** of the devices...disposed on a **two-** with...permits the **power supply** of devices...disposed on a **two-w** with...pole of the **power supply** source is...wire of the **two-wire** connected...the second (**floating** ground). The...
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3. Method for transmitting digital electrical signals
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...pole of the **power supply** is connected...wire of the **two-wire c** connected...pole of the **power supply** via a resistor...second wire The...located in a **two-wire communication** line with...pole of th first...
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4. MOVABLE SECURITY BAR ASSEMBLIES
COHEN-RAVID, Moshe / DAVIS, Neil / BOETZKES, Peter (RA COHEN-RAVID, Moshe ; DAVIS, Neil ; BOETZKES, Peter), *PAT APPLICATION*, Feb 2008
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...floatably mounted in the opening, and the **floating** movement o to...can sense the relative movement of the **floating** portion of the respect...the engaged transport mechanism form a **floating** linked mechanism assembly...
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5. DC Power supply circuit for line interface circuits
Ferry, Michel F. M. S. / Trehin, Jacques L. (International Bus Corporation), *UNITED STATES PATENT AND TRADEMARK OFFICE* 1985
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...affected. What is obtained is a **floating** circuit that is not perturb **power supply** circuits and, more particularly...or switching system **communication** lines. In some of these...invention to provide a si circuit that exhibits a...

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6. Single power supply battery feed circuit
Stahl, Douglas C. (GTE Automatic Electric Incorporated), *UN TRADEMARK OFFICE GRANTED PATENT*, Dec 1985
patno:US4558186
...an intermediate **power supply**, with a voltage of...of regulating this...blockdiagram of a single **power supply** battery feed circuit.. **communication** path having first...arrangement for said **two wir** comprising...
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KOMAREK, James A. / MINNEY, Jack L. / NORDINE, Stephen WADA, Richard / STOCKMAN, John F. (CREATIVE INTEGRATI *PATENT COOPERATION TREATY APPLICATION*, Oct 1994
patno:WO94023508
...available within the system, or alternatively some type of **power** the piezo driver to provide a higher...without the need of providing **power supply** so that sound volume improvement can be provide
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patno:US5548592
A telephone communication system for communicating between a telephone lines and a single common in-house two wire line is com coupling to the plurality of exterior telephone lines and for controlli
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...oscillator 100 and controlled by control circuit 59. When the VCO oscillating mode, a crystal and a **resistor** capacitor network is tied VCXO. The VCO oscillator is buffered with 102 and appears on outp
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2. Low noise solid-state thermostat with microprocessor controlled fa and programmable set points
Shearer, Jon Douglas, *UNITED STATES PATENT AND TRADEMAR PUBLICATION*, Jul 2005
patno:US20050150968
...to FIG. 1, a **low noise** solid-state...desired. An AC **power suppl** 54 via a **resistor** 56 included...circuit common (or **floating** grounc **resistor** 150 and capacitor...46 through a **resistor** 156 so that...c **communication** bus comprising...
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3. METHOD FOR TRANSMITTING DIGITAL ELECTRIC SIGNALS
Ovchinnikov, Valery Vasilievich (Ovchinnikov, Valery Vasilie
APPLICATION, Jun 2005
patno:EP1548954
...separate **power supply** of the devices...disposed on a **two-wire** with...source via a **resistor**, includes...permits the **power supply low noise** immunity...disposed on a **two-wire communication** li **resistor**, which includes...
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4. Method for transmitting digital electrical signals
Ovchinnikov, Valery Vasilievich, *UNITED STATES PATENT AND GRANT PUBLICATION*, Jul 2006
patno:US20060165159
...located in a **two-wire communication** line with...pole of the **pc** first...**supply** via a **resistor**, comprises...supports network **power** makes...method is its **low noise** immunity...located in a **two-wire** with...pole of the **power supply** and the first...**supply** via a **resis**
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26. 21191N.book [PDF-470K]
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...Resonators Used: 4.0 MHz 8.0 MHz 16.0 MHz Note 1: See Table
values of C1 and C2. 2: A series **resistor** (RS) may be required for
varies with the oscillator mode chosen. C1(1) C2(1) XTAL OSC2 OS
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is a national stage entry of PCT/RU03/00402 , International Filing Date: 09/15/2003
claims foreign priority to 2002125391 , filed 09/23/2002

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Assignments
Examiner Number: **80488 / TORRES, JUAN**
Filing or 371(c) Date: **03/18/2005** eDan
Group Art Unit: **2611** IFW Madras
Effective Date: **03/18/2005**
Class/Subclass: **375/219.000**
Application Received: **03/18/2005**
Lost Case: **NO**
Pat. Num./Pub. Num: **/20060165159**
Interference Number:
Issue Date: **00/00/0000**
Unmatched Petition: **NO**
Date of Abandonment: **00/00/0000**
L&R Code: Secrecy Code:**1**
Attorney Docket Number: **U 015515-6**
Third Level Review: **NO**
Secrecy Order: **NO**
Status: **71 /RESPONSE TO NON-FINAL OFFICE ACTION ENTERED AND FORWARDED TO EXAMINER**
Status Date: **06/05/2009**
Confirmation Number: **6771**
Oral Hearing: **NO**
Title of Invention: **METHOD FOR TRANSMITTING DIGITAL ELECTRICAL SIGNALS**

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Inventor Information for 10/528332

Inventor Name	City	State/Country
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10528332	Not Issued	20060165159	71	03/18/2005	Method for transmitting digital electrical signals	TORRES,JUAN	OVCH VALE VASI
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